ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

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Title of Invention STRUCTURE AND METHOD OF PATTERNING A MAGNETIC TUNNEL JUNCTION STACK FOR A MAGNETO-RESISTIVE RANDOM ACCESS MEMORY

Application Number:

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Confirmation Number:

First Named Applicant:

Michael Gaidis

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Examiner: D. LE

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20030181056).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

in	it	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
4	Z	1	4619872	1986-10-28	Goor, et al.	<u> </u>		
12	7	2	6365419	2002-04-02	Durlam, et al.	,		
-17		3	6495275	2002-12-17	Kamiguchi, et al.			
		4	6548849	2003-04-15	Pan, et al.			

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

	init,	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
\Box	10/	1	20020009616	2002-01-24	Kamiguchi, et al.			
7	1	2	20020048127	2002-04-25	Fukuzawa, et al.			
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Signature

Examiner Name	Date
26	9/2005